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Substitute for form 1449A/PTO							$\neg$	Application Nun		10/660,232		
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT							F	irst Named Inve	entor:	Gelu Voicu		
(Use as many sheets as necessary)								Examiner: Lam	T. Mai	GROUP: 2819		
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Ø	B0		US-2004/0108844		A1		06/2004	Stanescu et a	l.			
Ø	B02	?	US-	5,717,3	23			02/1998	Tailliet			
Q	B03	}	US-6,201,491		B1		03/2001	DO1 Brunolli et al.				
Ø	B04	B04 US-6,32		US-6,320,451		B1		11/2001	Harvey et al.			
Ø	B05	B05		US-6,555,996		B2		04/2003	Drori et al.			
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D	A05	US-5	US-5,297,056				03/22/1994	Lee et al.			
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Q	Catal pp.1-	yst Semi								D. Reference Inputs," 1	997,
Q	Catalyst Semiconductor, Inc., "CAT524: 8-Bit Quad Digital POT," 1998, pp. 25-36										
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ø	Catal: Indep	yst Semicendent R	conduct eference	or, Inc.	, "CA'	Γ5111 99, pp	, CAT5112, CAT . 1-6	'5113 and CAT	75114: 100/32-7	Fap Digital POT with	
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Ø	A07	U	S-5;717,3	323			02/1998	Tailliet				
Ø	A08	U	US-6,201,491		Bl		03/13/2001	Brunolli et al				
<b>Q</b>	A09	U	US-6,320,451				11/2001	Harvey et al.				
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Ų	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)  Stanescu et al., "32 Taps Digitally Controlled Buffered Potentiometer," Proceedings of CAS 2001 International Semiconductor Conference, IEEE Catalog Number 01TH8547, pp. 535-538, October 2001											
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